



Features:

- RoHS Compliant (Pb-Free, LVDS Compatible Signals)
- Inherent Low Power and Low EMI Emission
- Low Phase Jitter with New Generation PLL Design
- Complimentary Output, Tri-State Enable/Disable Standard
- 7 x 5 x 2 mm Ceramic SMD Package

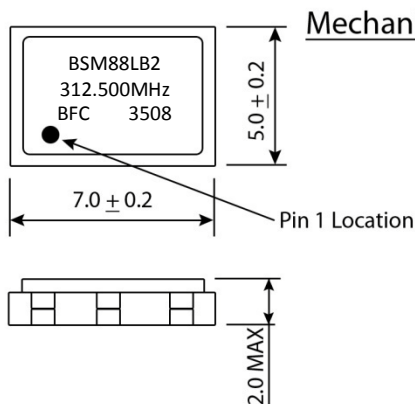


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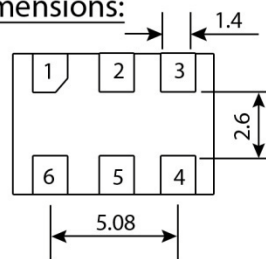
ELECTRICAL SPECIFICATIONS	
Frequency Range (MHz)	300.0 to 700 MHz
Input Voltage	3 = +3.3 VDC ± 5%
Input Current	80 mA Maximum
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	± 100ppm; ±50 ppm; ± 25ppm
Temperature Range	0°C to +70°C; -40°C to +85°C
Standard Stability	±50ppm / 0°C to +70°C
Duty Cycle	Tristate 55/45% symmetry
Output Load	100 Ohms Across Differential Outputs (Offset 1.25V Typical)
Logic "1" / Logic "0" Level	+1.43V typical / +1.10V typical
Rise and Fall Time (Tr/Tf)	1.0 ns Max. at 20% to 80% Vp-p
Start Up Time	5 ms Max.
Phase Jitter (RMS, 1 Sigma)	1.6 ps Typical for fj = 12 KHz to 20 MHz, at 622.080 MHz
Tristate Function	Input (Pin 1) High (>2.2V) or Open: Output (Pin 4,5) Active Input (Pin 1) Low (<0.4 V): Output Disabled in High Impedance
Enable / Disable Time	100 ns Maximum

Part Number Table				
Model	Duty Cycle	Stability	Temp. Range (°C)	Frequency
BSM88L	Blank = 60 / 40%	B = ± 100ppm	2 = 0° ~ 70°C	In MHz
	S = 55 / 45%	C = ± 50ppm	5 = -40° ~ 85°C	
		E = ± 25ppm		

Part Number Example				
BSM88L		B	2	312.500 MHz

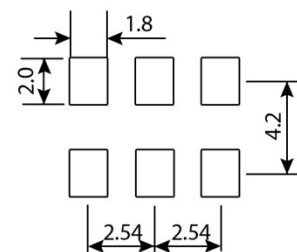


Mechanical Dimensions:



Pin Connections
 #1: E/D or NC #2: N/C
 #3: Ground, Case #4: Output
 #5: Compl-Output #6: Vcc

Recommended Solder Pad Layout



All dimensions are typical unless otherwise specified

Dimensions in Millimeters