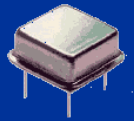


B500W SERIES

HCMOS/TTL CLOCK OSCILLATOR

WIDE TEMPERATURE RANGE



Features:

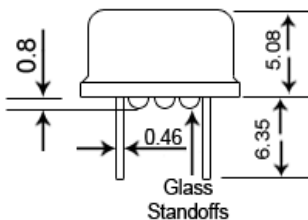
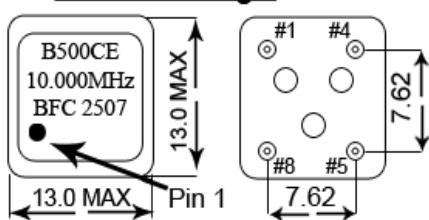
- **RoHS Compliant**
- Tight Frequency Stability over Extended Operating Temperature Range / COTS
- Very Low Phase Jitter with Fundamental or 3rd Overtone Crystal Design
- Tri-state Output Available, Hermetically Sealed, Industry Standard Lead Spacing
- Thru-hole PCB Applications in Environments Exposed to Temperature Extremes (-55°C to 105°C)

ELECTRICAL SPECIFICATIONS		
Frequency Range	1.0 to 60 MHz	
Input Voltage (Vcc)	+5VDC ± 10%; +3.3 VDC ± 10%	
Input Current	40 mA Maximum, depending on frequency and output load	
Storage Temperature	-55 to 125°C	
Supply Voltage	3.3V or 5Vdc ±5%, or 3.3V or 5Vdc ±10%	
Overall Frequency Stability	00 = ± 100 ppm / -55°C to 105°C	
	50 = ± 50 ppm / -55°C to 105°C	
Symmetry	60/40%, 55/45%	
Output Load	HCMOS:	Drive up to 50 pF load
	TTL:	Drive up to 10 TTL gates
Logic "1"	0.9 Vcc Minimum	
Logic "0"	0.1 Vcc Maximum	
Rise / Fall Time (Tr/Tf)	10 ns Maximum	
Start-up Time	10 ms Maximum	
Phase Jitter	1 ps Maximum at 1Σ for fj > 1 kHz	
Aging	3 ppm First year; 1 ppm/year after the first year	
Tristate Function	Input (Pin 1) High (> 2.2V) or open: Output (Pin 8) active Input (Pin 1) Low (< 0.8V): Output disabled in high impedance	
Enable Time	100 ns Maximum	

Notes - Serialized temperature test data available at additional cost

Part Number Table						
Model	Frequency Stability	Operating Temp. (°C)	Symmetry	Tristate	Voltage	Frequency
B5	50 = 50 ppm	W = -55°C to 105°C	Blank = 60/40%	Blank = No Tristate	Blank = 5V	In MHz
	00 = 100 ppm		S = 55/45%	E = Enable PIN # 1	3 = 3.3V	
Part Number Example						
B5	00	W	S	E	3	3.000MHz

DIL-8 Package



Pin Connections
 #1: E/D or NC
 #4: Ground
 #5: Output
 #8: Vcc

DIL-8 Gull Wing Option

